

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

| APPLICATION NO | D. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. | |
|----------------|----------|---------------|----------------------|------------------------|------------------|--|
| 10/666,462 | | 09/19/2003 | Brent Stone | 42P16890 | 8351 | |
| 8791 | 7590 | 03/24/2006 | | EXAM | EXAMINER | |
| | | LOFF TAYLOR & | HYEON, HAE M | | | |
| SEVENTI | | OULEVARD | | ART UNIT | PAPER NUMBER | |
| LOS ANG | ELES, CA | A 90025-1030 | | 2839 | | |
| | | | | DATE MAILED: 03/24/200 | 6 | |

Please find below and/or attached an Office communication concerning this application or proceeding.

| | Application No. | Applicant(s) | |
|--|--|--|-------|
| Office Action Summany | 10/666,462 | STONE ET AL. | |
| Office Action Summary | Examiner | Art Unit | |
| | Нае М. Нуеоп | 2839 | |
| The MAILING DATE of this communication app Period for Reply | ears on the cover sheet with the c | orrespondence address | |
| A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). | ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tim rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI | l. lely filed the mailing date of this communication. O (35 U.S.C. § 133). | |
| Status | | | |
| 1)⊠ Responsive to communication(s) filed on 06 Ma | arch 2006. | | |
| , | action is non-final. | | |
| 3) Since this application is in condition for allowan | secution as to the merits is | | |
| closed in accordance with the practice under E | • | | |
| Disposition of Claims | | | |
| 4)⊠ Claim(s) <u>1-16</u> is/are pending in the application. | | | |
| 4a) Of the above claim(s) is/are withdraw | | | |
| 5) Claim(s) is/are allowed. | | | |
| 6)⊠ Claim(s) <u>1-16</u> is/are rejected. | | | |
| 7) Claim(s) is/are objected to. | | | |
| 8) Claim(s) are subject to restriction and/or | election requirement. | | |
| o) Claim(s) are subject to restriction and si | ologion roquiroment. | | |
| Application Papers | | | |
| 9)☐ The specification is objected to by the Examine | r. | | |
| 10) The drawing(s) filed on is/are: a) □ acce | epted or b) \square objected to by the ${	t E}$ | Examiner. | |
| Applicant may not request that any objection to the | drawing(s) be held in abeyance. See | 37 CFR 1.85(a). | |
| Replacement drawing sheet(s) including the correcti | ion is required if the drawing(s) is obj | ected to. See 37 CFR 1.121(d) | |
| 11) ☐ The oath or declaration is objected to by the Ex | aminer. Note the attached Office | Action or form PTO-152. | |
| Priority under 35 U.S.C. § 119 | | | y . I |
| 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list | s have been received. s have been received in Application ity documents have been received (PCT Rule 17.2(a)). | on No ed in this National Stage | |
| Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date | 4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other: | | |

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- Claims 1-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Singh et al 2. (US 6,885,102 B2) in view of Kajinuma (US 6,722,910 B2).

Singh discloses an apparatus comprising a printed circuit board (PCB), a motherboard; a connector 12 having a ball grid array (BGA) mounted on the PCB; and an integrated circuit (IC) package, microelectronic dies, for insertion into the connector 12. In the Background of the invention, Singh discloses that the apparatus transmits signals and provides power or ground to the circuit of the apparatus through BGA (see column 1, lines 16-32). Therefore, it is clear that the IC package has a plurality of input/output (I/O) terminals, which is BGA. Furthermore, Figure 1 of Singh, which is a partial view of the substrate 12 shown in Figure 2, discloses that the contacts A, U and X have horizontal and vertical pitches that are different from the other contacts. Therefore, the apparatus of Singh has the plurality of I/O terminals with a varied pitch distance. However, Singh does not disclose the use of land pads, land grid array (LGA) or pins as the terminals of the IC package. Singh only discloses BGA terminals.

Kajinuma discloses a zero insertion force (ZIP) IC socket connector 1 having a plurality of pin terminals 40 for connection with a plurality of pin terminals of an IC package.

Art Unit: 2839

Furthermore, a land grid array (LGA), a pin grid array (PGA) and a ball grid array (BGA) are well known IC package terminals in the art of an electrical connector.

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to modify the apparatus taught by Singh such that it would have pin terminals as taught by Kajinuma because pins or LGA terminals are simply other types of terminals that are available to use with an IC package.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

US Patent 5,437,556 by Bargain et al., US Patent No. 6,558,181 B2 by Chung et al. and US Patent No. 6,848,936 B2 by DeFord.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hae M. Hyeon whose telephone number is 571-272-2093. The examiner can normally be reached on Mon.-Fri. (8:30-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tulsidas C. Patel can be reached on (571) 272-2098. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2839

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hae M Hyeon Primary Examiner Art Unit 2839

hmh hmh

Hae Moon Hyeon